

WEST Search History

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DATE: Monday, July 11, 2005

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
		<i>DB=PGPB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L1	plurality adj backplane and management adj processor near cards and interconnecting near group	0

END OF SEARCH HISTORY

Refine Search

Search Results -

Term	Documents
MANAGEMENT	237424
MANAGEMENTS	652
PROCESSOR	430460
PROCESSORS	139534
(28 AND (MANAGEMENT ADJ PROCESSOR)).PGPB,USPT.	0
(L28 AND MANAGEMENT ADJ PROCESSOR).PGPB,USPT.	0

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L29

Refine Search

Recall Text

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Interrupt

Search History

DATE: Monday, July 11, 2005 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	DB=PGPB,USPT; PLUR=YES; OP=ADJ		
<u>L29</u>	L28 and management adj processor	0	<u>L29</u>
<u>L28</u>	data near interconnect near group	2	<u>L28</u>
<u>L27</u>	plurality adj backplanes and plurality adj modules and group near interconnect	0	<u>L27</u>
<u>L26</u>	plurality adj backplanes and plurality adj cards and group near interconnect	0	<u>L26</u>
<u>L25</u>	plurality adj backplanes and plurality adj cards and group near interconnecting	0	<u>L25</u>

<u>L24</u>	L23 and card and backplane	1	<u>L24</u>
<u>L23</u>	interconnect and group near slots and management adj processor	2	<u>L23</u>
<u>L22</u>	interconnect near bundle and management adj processor	0	<u>L22</u>
<u>L21</u>	L20 and cards	13	<u>L21</u>
<u>L20</u>	L19 and backplane	18	<u>L20</u>
<u>L19</u>	interconnect near group	422	<u>L19</u>
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<u>L18</u>	interconnect near group and management adj processor	0	<u>L18</u>
<u>L17</u>	L15 and interconnect near cluster	0	<u>L17</u>
<u>L16</u>	L15 and interconnect near group	0	<u>L16</u>
<u>L15</u>	management adj processor and plurality adj cards	4	<u>L15</u>
<u>L14</u>	L13 and cards	12	<u>L14</u>
<u>L13</u>	L12 and interconnect near group	17	<u>L13</u>
<u>L12</u>	backplane and group	3992	<u>L12</u>
<u>L11</u>	L10 and processor near routing	3	<u>L11</u>
<u>L10</u>	l3 and cards	59	<u>L10</u>
<u>L9</u>	L8 and cards	4	<u>L9</u>
<u>L8</u>	L3 and cluster	4	<u>L8</u>
<u>L7</u>	L6 and group	2	<u>L7</u>
<u>L6</u>	L3 and plurality adj cards	2	<u>L6</u>
<u>L5</u>	L3 and link near group	0	<u>L5</u>
<u>L4</u>	L3 and group adj cables	0	<u>L4</u>
<u>L3</u>	backplane and management adj processor	66	<u>L3</u>
<u>L2</u>	L1 and backplane	0	<u>L2</u>
<u>L1</u>	bundle adj cables and plurality near cards	7	<u>L1</u>

END OF SEARCH HISTORY

Freeform Search

Database:	US Pre-Grant Publication Full-Text Database
	US Patents Full-Text Database
	US OCR Full-Text Database
	EPO Abstracts Database
	JPO Abstracts Database
	Derwent World Patents Index
	IBM Technical Disclosure Bulletins

Term:	L28 and management adj processor
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Display:	<input type="text" value="30"/> Documents in <u>Display Format:</u> <input type="text" value="CIT"/> Starting with Number <input type="text" value="1"/>
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Generate: ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

Search

Clear

Interrupt

Search History

DATE: Monday, July 11, 2005 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>		
<u>L29</u>	L28 and management adj processor	0	<u>L29</u>
<u>L28</u>	data near interconnect near group	2	<u>L28</u>
<u>L27</u>	plurality adj backplanes and plurality adj modules and group near interconnect	0	<u>L27</u>
<u>L26</u>	plurality adj backplanes and plurality adj cards and group near interconnect	0	<u>L26</u>
<u>L25</u>	plurality adj backplanes and plurality adj cards and group near interconnecting	0	<u>L25</u>
<u>L24</u>	L23 and card and backplane	1	<u>L24</u>
<u>L23</u>	interconnect and group near slots and management adj processor	2	<u>L23</u>
<u>L22</u>	interconnect near bundle and management adj processor	0	<u>L22</u>
<u>L21</u>	L20 and cards	13	<u>L21</u>
<u>L20</u>	L19 and backplane	18	<u>L20</u>
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	<i>DB=USPT; PLUR=YES; OP=ADJ</i>		

<u>L18</u>	interconnect near group and management adj processor	0	<u>L18</u>
<u>L17</u>	L15 and interconnect near cluster	0	<u>L17</u>
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<u>L8</u>	L3 and cluster	4	<u>L8</u>
<u>L7</u>	L6 and group	2	<u>L7</u>
<u>L6</u>	L3 and plurality adj cards	2	<u>L6</u>
<u>L5</u>	L3 and link near group	0	<u>L5</u>
<u>L4</u>	L3 and group adj cables	0	<u>L4</u>
<u>L3</u>	backplane and management adj processor	66	<u>L3</u>
<u>L2</u>	L1 and backplane	0	<u>L2</u>
<u>L1</u>	bundle adj cables and plurality near cards	7	<u>L1</u>

END OF SEARCH HISTORY